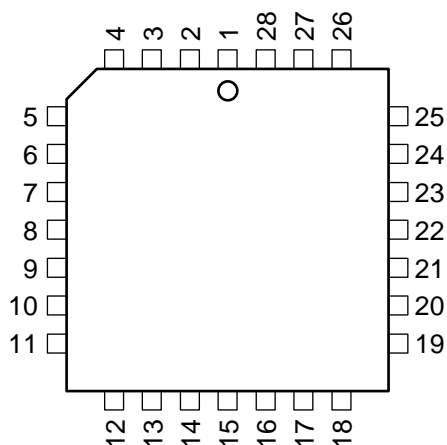


DS3/E3/STS-1 LINE INTERFACE WITH RECEIVE EQUALIZER

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I	LIN+	15	I	TNEG
2	—	NC	16	I	TCLK
3	I	LIN-	17	—	Vcc
4	—	NC	18	I	OPT2
5	I	RFO	19	I	LF1
6	—	GND	20	I	LF2
7	—	Vcc	21	—	NC
8	—	GND	22	—	GND
9	O	LOUT+	23	O	RCLK
10	—	NC	24	O	RNEG
11	O	LOUT-	25	O	RPOS
12	I	LBO	26	—	Vcc
13	I	OPT1	27	O	LOWSIG
14	I	TPOS	28	I	CPD

INPUTS

CPD	: EXTERNAL CAPACITOR
LBO	: TRANSMITTER LINE BUILDOUT CONTROL
LF1, LF2	: FILTER NETWORK
LIN+, LIN-	: DIFFERENTIAL SIGNAL
OPT1, OPT2	: TRANSMIT OPTION 1, 2
RFO	: EXTERNAL REGISTER
TCLK	: TRANSMITTER CLOCK
TNEG	: TRANSMITTER NEGATIVE DATA
TPOS	: TRANSMITTER POSITIVE DATA

OUTPUTS

LOUT+	: POSITIVE DATA PULSE
LOUT-	: NEGATIVE DATA PULSE
LOWSIG	: LOW SIGNAL
RCLK	: RECOVERED CLOCK
RNEG	: RECEIVER NEGATIVE PULSE
RPOS	: RECEIVER POSITIVE PULSE

OTHER

NC	: NO CONNECT
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